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PATENT, TRADEMARK AND COPYRIGHT LAW

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Date: April 22, 2004

Attorney Docket No. H-1139

CUSTOMER NO. 24956

To: Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir: Transmitted herewith for filing is the patent application of:

Inventor: A. HAYASHI et al (See attached)

Deposit Account No. 50-1417.

A SEMICONDUCTOR INTEGRATED CIRCUIT CONTROLLING **OUTPUT IMPEDANCE AND SLEW RATE** 

Enclosed are:									
x	Sheets of Drawings								
X Th	This application is being filed without an executed Declaration.								
	Priority is claimed from Japanese Application No. 2003-118528 (which is hereby filed April 23, 2003 . A certified copy is attached herewith. incorporated								
1 X 1 —	Copies of the disclosure documents listed on the attached PTO 1449 form and  ignormal by reference)  ignormal discussed in the specification or ignormal attached Information Disclosure Statement.								
Sn	Small entity status under 37 CFR 1.9 and 1.27 is hereby claimed.								
X Sp	ecification: Abs	tract X	, Description	56_ pa	iges; and	27_c	:laim(s).		
Pre	eliminary Amendr	ment.							
Ex	ecuted Declaration	on.						-	
The filing fee i	s calculated as s	hown below:	Sm	all Entity		Larg	ge Entity		
For:	No. Filed	No. Extra	Rate	Fee	OR	Rate	Fee		
Basic Fee				\$ 375	]		\$ 7	70	
Total Claims	27 -20=	• 7	x 9	\$	]	x 18	\$ 1:	26	
Indep Claims	3 - 3 =	• 0	x 42	\$		x 84	\$	0	
Multiple Dependent Claim (s)			+ 140	\$		+ 280	\$	0	
<ul> <li>If difference is less than zero then enter '0' in second column</li> </ul>			Total	\$	]	Total	\$ 8	96	
X A	check or credit c	ard payment fo	orm in the amou	nt of \$ <u>896.</u>	00	is	enclosed f	or the filing fee.	
	e Commissioner		orized to charge	any additional	fees that r	nay be require	ed to		

Respectfully Submitted,

John R. Mattingly Registration No.

## LIST OF INVENTORS' NAMES AND ADDRESSES

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Takemi NEGISHI, Honjo, JAPAN;

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